



PBSS4230T

30 V; 2 A NPN low V_{CEsat} transistor

4 August 2025

Product data sheet

1. General description

Low V_{CEsat} transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5230T

2. Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High efficiency leading to less heat generation
- Reduced printed-circuit board requirements
- Cost effective alternative to MOSFETs in specific applications
- AEC-Q101 qualified

3. Applications

- Power management
 - DC/DC conversion
 - Supply line switching
 - Battery charger
 - LCD backlighting
- Peripheral driver
 - Driver in low supply voltage applications (e.g. lamps and LEDs)
 - Inductive load drivers (e.g. relays, buzzers and motors)

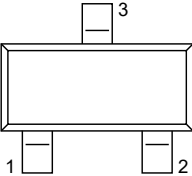
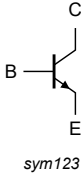
4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	30	V
I_C	collector current		-	-	2	A
h_{FE}	DC current gain	$V_{CE} = 2 \text{ V}; I_C = 100 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	350	470	-	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym123
2	E	emitter		
3	C	collector		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4230T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4230T	% 3D

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	30	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current	single pulse; t _p ≤ 1 ms		-	2	A
I _{CM}	peak collector current			-	3	A
I _{BM}	peak base current			-	300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W
			[2]	-	-	260	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I _{CBO}	collector-base cut-off current	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
		V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	50	µA
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 100 mA; T _{amb} = 25 °C		350	470	-	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C		300	450	-	
		V _{CE} = 2 V; I _C = 1 A; T _{amb} = 25 °C		300	420	-	
		V _{CE} = 2 V; I _C = 2 A; T _{amb} = 25 °C		150	250	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 1 mA; T _{amb} = 25 °C		-	45	70	mV
		I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C		-	70	100	mV
		I _C = 750 mA; I _B = 15 mA; T _{amb} = 25 °C		-	120	180	mV
		I _C = 1 A; I _B = 50 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	130	180	mV
		I _C = 2 A; I _B = 200 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	240	320	mV
R _{CEsat}	collector-emitter saturation resistance	I _C = 500 mA; I _B = 50 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	140	200	mΩ
V _{BEsat}	base-emitter saturation voltage	I _C = 2 A; I _B = 200 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	-	1.1	V
V _{BEon}	base-emitter turn-on voltage	V _{CE} = 2 V; I _C = 100 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C		-	-	0.75	V
f _T	transition frequency	V _{CE} = 10 V; I _C = 100 mA; f = 100 MHz; T _{amb} = 25 °C		100	230	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	15	20	pF

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 -Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

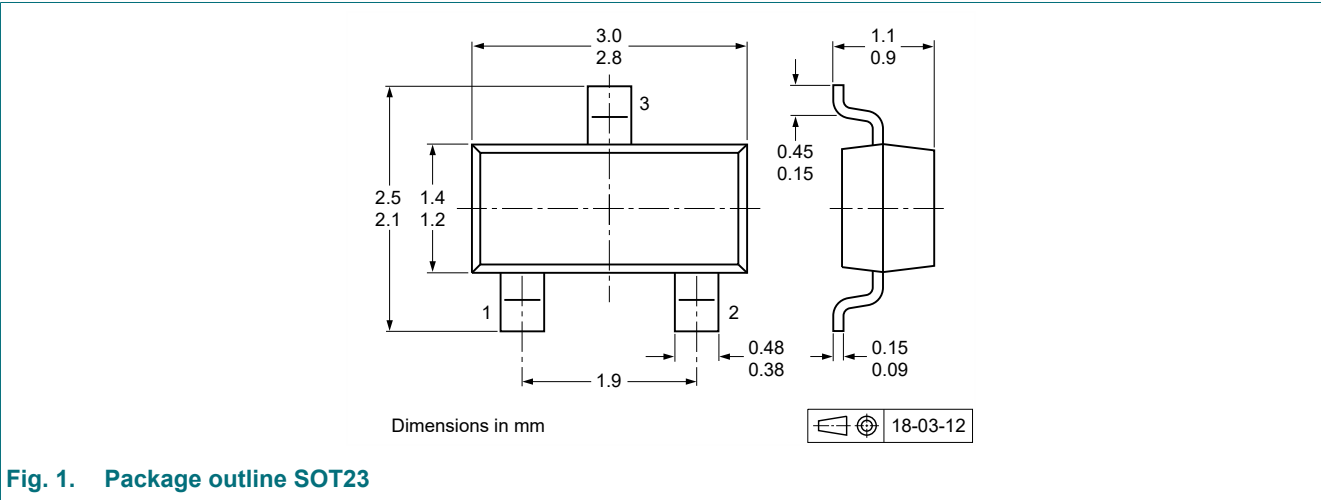


Fig. 1. Package outline SOT23

13. Soldering

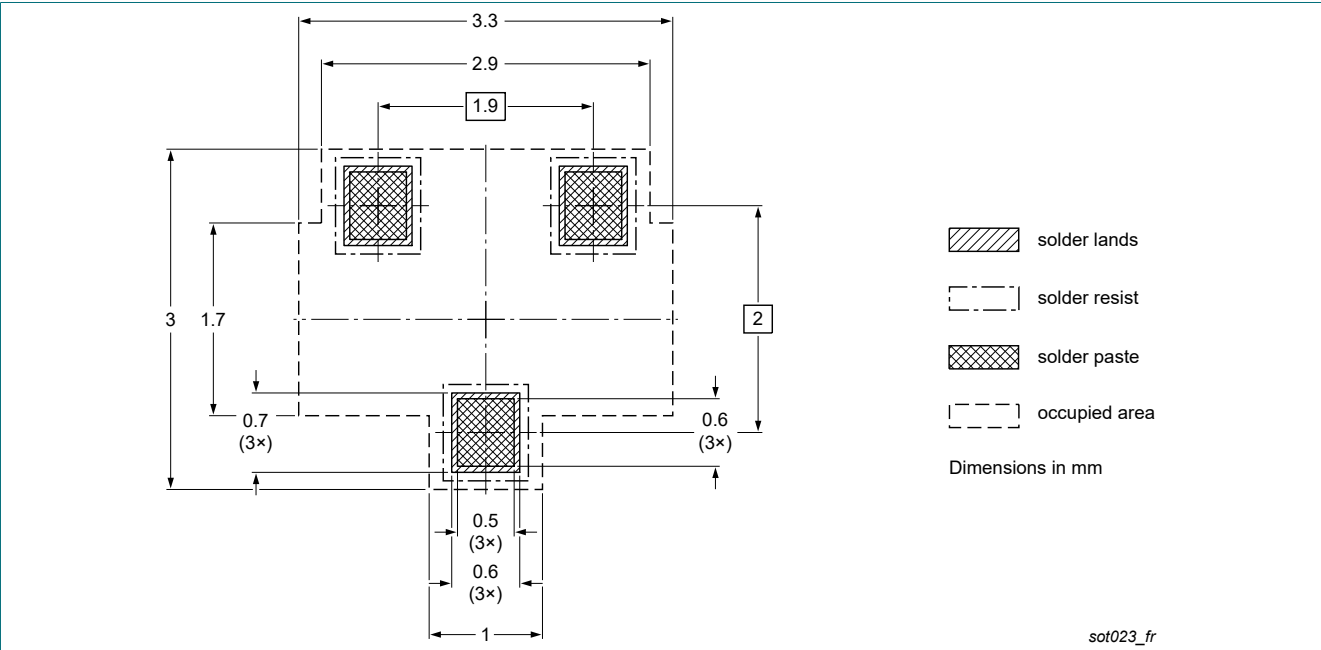


Fig. 2. Reflow soldering footprint for SOT23

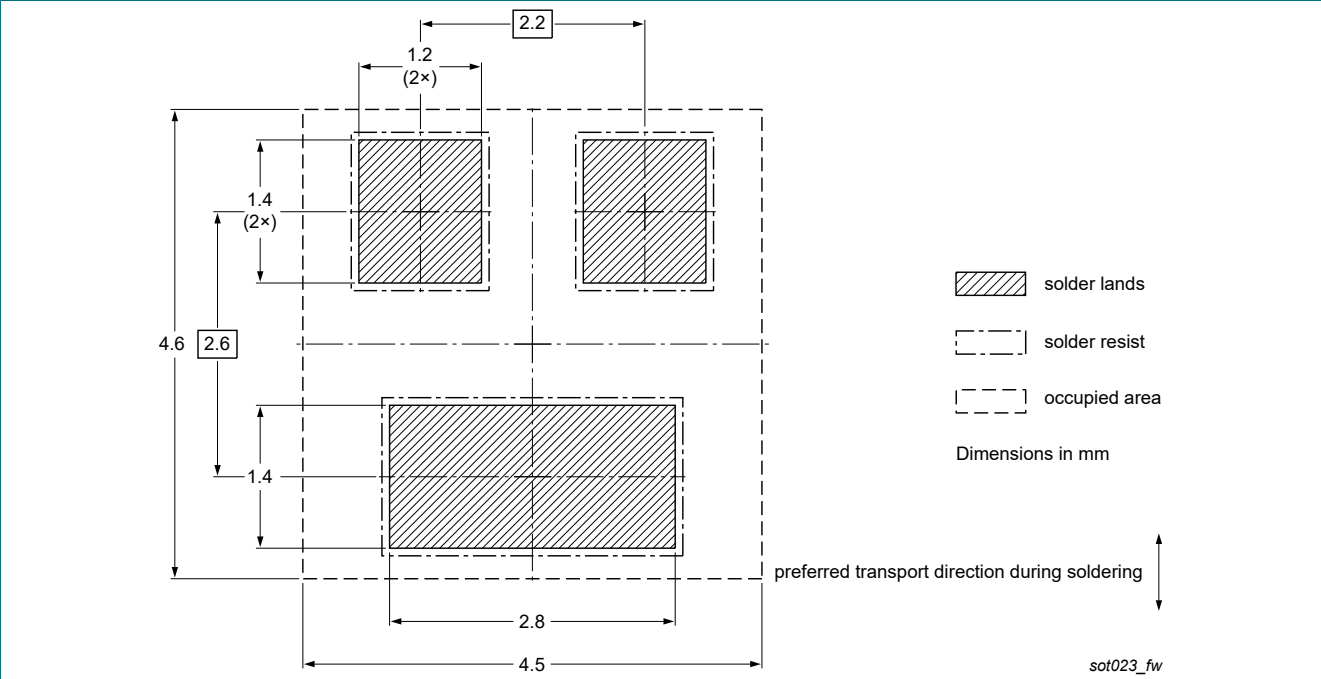


Fig. 3. Wave soldering footprint for SOT23

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4230T v.2	20250804	Product data sheet	-	PBSS4230T v.1
Modifications	<ul style="list-style-type: none">The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.Legal texts have been adapted to the new company name where appropriate.			
PBSS4230T v.1	20170207	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description..... 1

2. Features and benefits..... 1

3. Applications..... 1

4. Quick reference data..... 1

5. Pinning information.....2

6. Ordering information.....2

7. Marking.....2

8. Limiting values..... 2

9. Thermal characteristics..... 3

10. Characteristics..... 3

11. Test information..... 3

12. Package outline..... 4

13. Soldering..... 5

14. Revision history.....6

15. Legal information.....7

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