



# PBSS4320T

20 V NPN low V<sub>CEsat</sub> transistor

4 August 2025

Product data sheet

## 1. General description

NPN low V<sub>CEsat</sub> transistor in a SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5320T

## 2. Features and benefits

- Low collector-emitter saturation voltage V<sub>CEsat</sub> and corresponding low R<sub>CEsat</sub>
- High collector current capability
- High collector current gain
- Improved efficiency due to reduced heat generation
- AEC-Q101 qualified

## 3. Applications

- Power management applications
- Low and medium power DC/DC convertors
- Supply line switching
- Battery chargers
- Linear voltage regulation with low voltage drop-out (LDO)

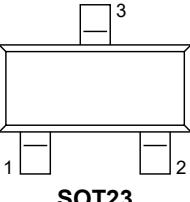
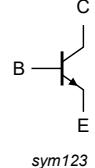
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	20	V
I <sub>C</sub>	collector current		-	-	2	A
I <sub>CRM</sub>	repetitive peak collector current	$\delta \leq 0.25$ ; $t_p \leq 100$ ms	-	-	3	A
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C = 2$ A; $I_B = 200$ mA; pulsed; $t_p \leq 300$ $\mu$ s; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C	-	80	105	mΩ

## 5. Pinning information

**Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	 SOT23	 sym123
2	E	emitter		
3	C	collector		

## 6. Ordering information

**Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PBSS4320T	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

## 7. Marking

**Table 4. Marking codes**

Type number	Marking code <sup>[1]</sup>
PBSS4320T	ZG%

[1] % = placeholder for manufacturing site code

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit	
$V_{CBO}$	collector-base voltage	open emitter	-	20	V	
$V_{CEO}$	collector-emitter voltage	open base	-	20	V	
$V_{EBO}$	emitter-base voltage	open collector	-	5	V	
$I_C$	collector current		-	2	A	
$I_{CRM}$	repetitive peak collector current	$\delta \leq 0.25$ ; $t_p \leq 100$ ms	-	3	A	
$I_{CM}$	peak collector current	single pulse; $t_p \leq 1$ ms	-	5	A	
$I_B$	base current		-	0.5	A	
$P_{tot}$	total power dissipation	$T_{amb} \leq 25$ °C	[1]	-	mW	
			[2]	-	480	mW
			[3]	-	540	mW
			[1] [4]	-	1.2	W
$T_j$	junction temperature		-	150	°C	
$T_{amb}$	ambient temperature		-65	150	°C	

Symbol	Parameter	Conditions		Min	Max	Unit
$T_{stg}$	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
 [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.  
 [3] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.  
 [4] Operated under pulsed conditions:  $t_p \leq 100$  ms; duty cycle  $\delta \leq 0.25$ .

## 9. Thermal characteristics

**Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W
			[2]	-	-	260	K/W
			[3]	-	-	230	K/W
			[4]	-	-	104	K/W

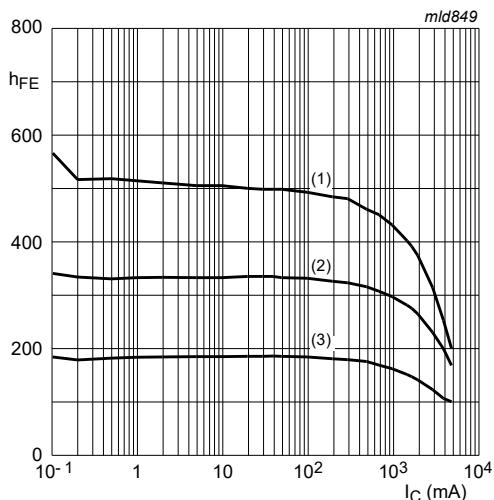
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.  
 [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.  
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
 [4] Operated under pulsed conditions: pulse width  $t_p \leq 100$  ms;  $\delta \leq 0.25$ .

## 10. Characteristics

**Table 7. Characteristics**

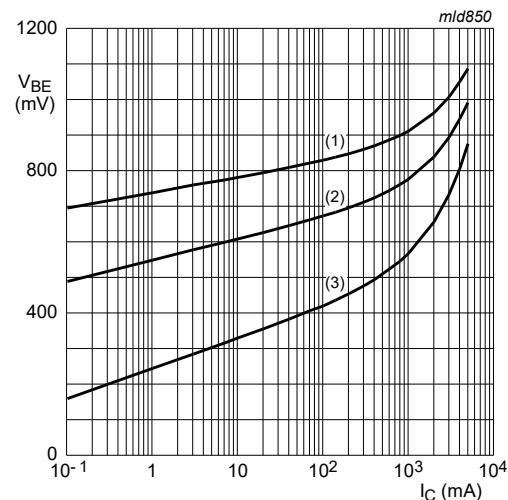
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{CBO}$	collector-base cut-off current	$V_{CB} = 20$ V; $I_E = 0$ A; $T_{amb} = 25$ °C		-	-	100	nA
		$V_{CB} = 20$ V; $I_E = 0$ A; $T_j = 150$ °C		-	-	50	µA
$I_{EBO}$	emitter-base cut-off current	$V_{EB} = 5$ V; $I_C = 0$ A; $T_{amb} = 25$ °C		-	-	100	nA
$h_{FE}$	DC current gain	$V_{CE} = 2$ V; $I_C = 100$ mA; $T_{amb} = 25$ °C		220	-	-	
		$V_{CE} = 2$ V; $I_C = 500$ mA; $T_{amb} = 25$ °C		220	-	-	
		$V_{CE} = 2$ V; $I_C = 1$ A; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		220	-	-	
		$V_{CE} = 2$ V; $I_C = 2$ A; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		200	-	-	
		$V_{CE} = 2$ V; $I_C = 3$ A; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		150	-	-	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 500$ mA; $I_B = 50$ mA; $T_{amb} = 25$ °C		-	-	70	mV
		$I_C = 1$ A; $I_B = 50$ mA; $T_{amb} = 25$ °C		-	-	120	mV
		$I_C = 2$ A; $I_B = 40$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	-	230	mV
		$I_C = 2$ A; $I_B = 200$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	-	210	mV
		$I_C = 3$ A; $I_B = 300$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	-	310	mV
$R_{CEsat}$	collector-emitter saturation resistance	$I_C = 2$ A; $I_B = 200$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	80	105	mΩ
$V_{BEsat}$	base-emitter saturation voltage	$I_C = 2$ A; $I_B = 40$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	-	1.1	V
		$I_C = 3$ A; $I_B = 300$ mA; pulsed; $t_p \leq 300$ µs; $\delta \leq 0.02$ ; $T_{amb} = 25$ °C		-	-	1.2	V

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{BEon}$	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}$ ; $I_C = 1 \text{ A}$ ; pulsed; $t_p \leq 300 \mu\text{s}$ ; $\delta \leq 0.02$ ; $T_{amb} = 25^\circ\text{C}$		1.2	-	-	V
$f_T$	transition frequency	$V_{CE} = 5 \text{ V}$ ; $I_C = 100 \text{ mA}$ ; $f = 100 \text{ MHz}$ ; $T_{amb} = 25^\circ\text{C}$		100	-	-	MHz
$C_c$	collector capacitance	$V_{CB} = 10 \text{ V}$ ; $I_E = 0 \text{ A}$ ; $i_e = 0 \text{ A}$ ; $f = 1 \text{ MHz}$ ; $T_{amb} = 25^\circ\text{C}$		-	-	35	pF



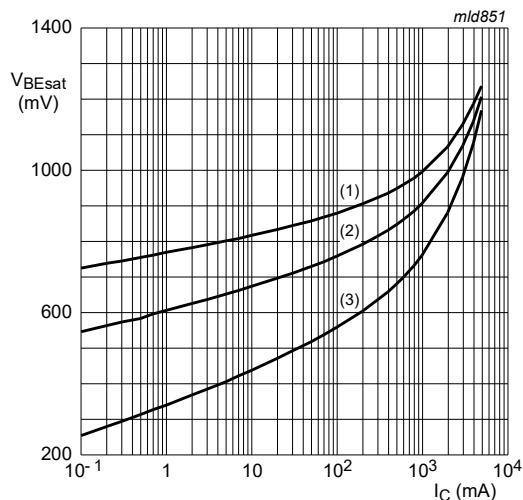
$V_{CE} = 2 \text{ V}$   
(1)  $T_{amb} = 150^\circ\text{C}$   
(2)  $T_{amb} = 25^\circ\text{C}$   
(3)  $T_{amb} = -55^\circ\text{C}$

**Fig. 1. DC current gain as a function of collector current; typical values**



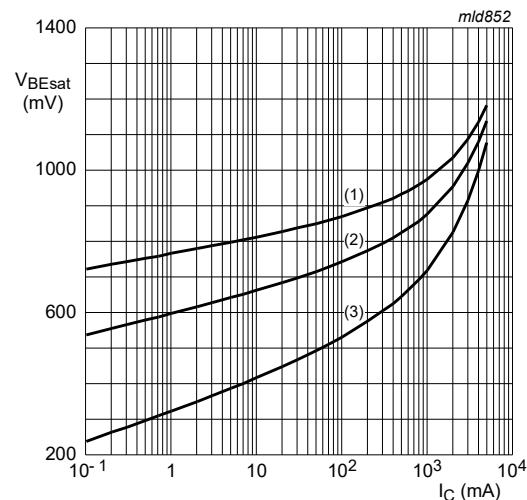
$V_{CE} = 2 \text{ V}$   
(1)  $T_{amb} = -55^\circ\text{C}$   
(2)  $T_{amb} = 25^\circ\text{C}$   
(3)  $T_{amb} = 150^\circ\text{C}$

**Fig. 2. Base-emitter voltage as a function of collector current; typical values**



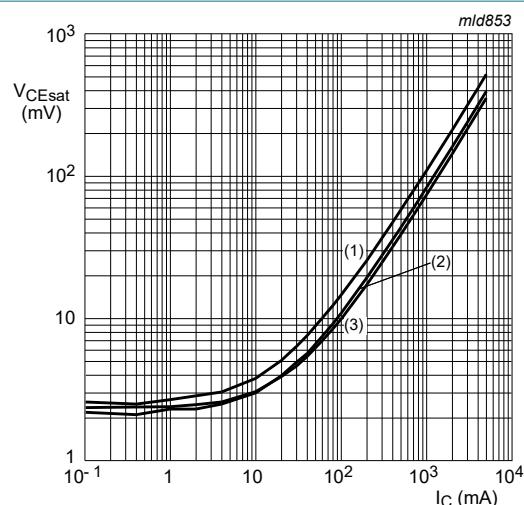
$I_C/I_B = 10$   
(1)  $T_{amb} = -55^\circ\text{C}$   
(2)  $T_{amb} = 25^\circ\text{C}$   
(3)  $T_{amb} = 150^\circ\text{C}$

**Fig. 3. Base-emitter saturation voltage as a function of collector current; typical values**



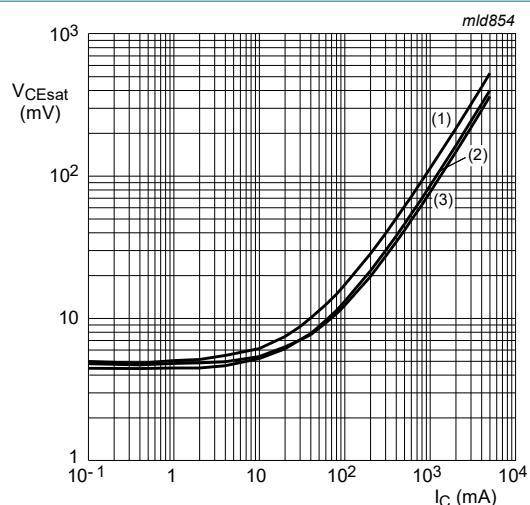
$I_C/I_B = 20$   
(1)  $T_{amb} = -55^\circ\text{C}$   
(2)  $T_{amb} = 25^\circ\text{C}$   
(3)  $T_{amb} = 150^\circ\text{C}$

**Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values**



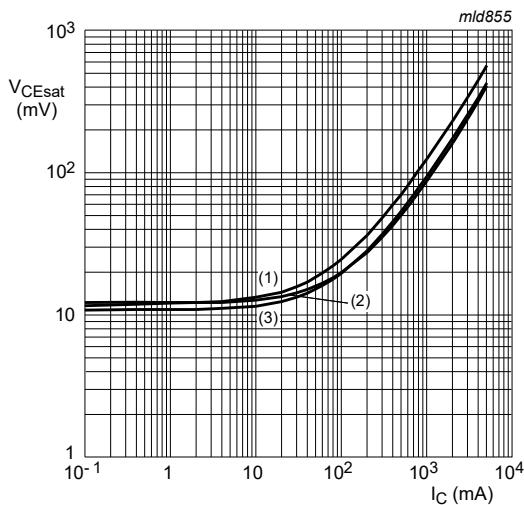
$I_C/I_B = 10$   
 (1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values**



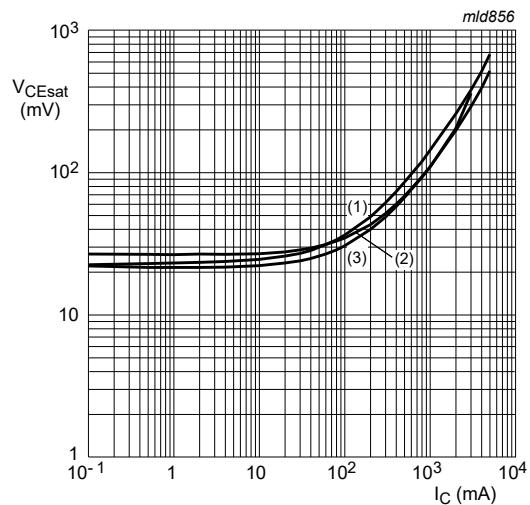
$I_C/I_B = 20$   
 (1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 6. Collector-emitter saturation voltage as a function of collector current; typical values**



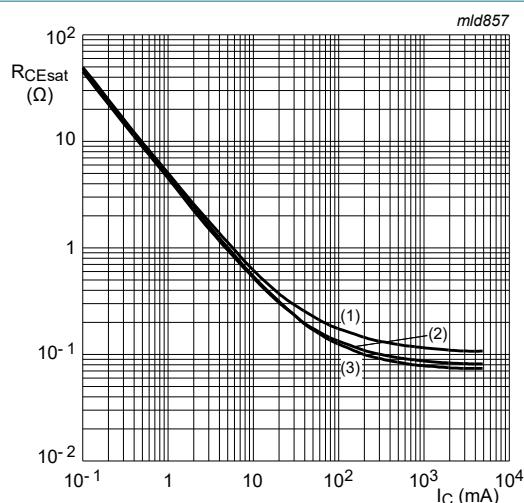
$I_C/I_B = 50$   
 (1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values**



$I_C/I_B = 100$   
 (1)  $T_{amb} = 150 \text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25 \text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55 \text{ }^{\circ}\text{C}$

**Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values**



$$\frac{I_C}{I_B} = 20$$

$$(1) T_{amb} = 150 \text{ } ^\circ\text{C}$$

$$(2) T_{amb} = 25 \text{ } ^\circ\text{C}$$

$$(3) T_{amb} = -55 \text{ } ^\circ\text{C}$$

Fig. 9. Equivalent on-resistance as a function of collector current: typical values

## 11. Test information

## Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

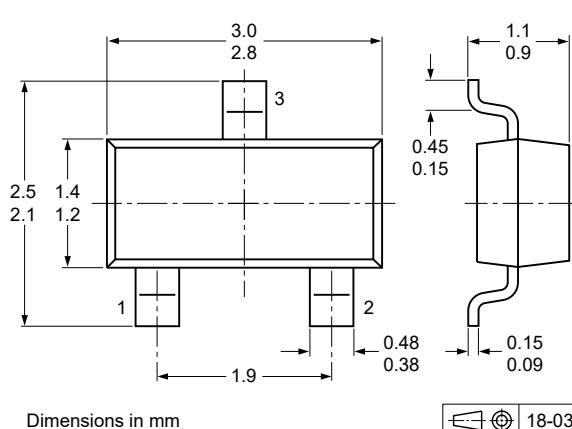


Fig. 10. Package outline SOT23

## 13. Soldering

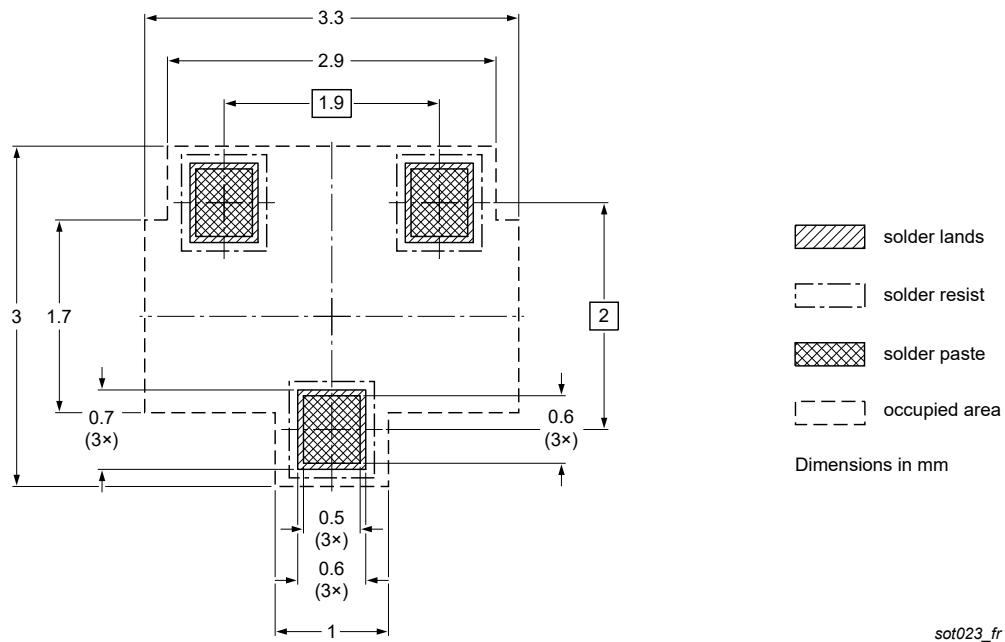


Fig. 11. Reflow soldering footprint for SOT23

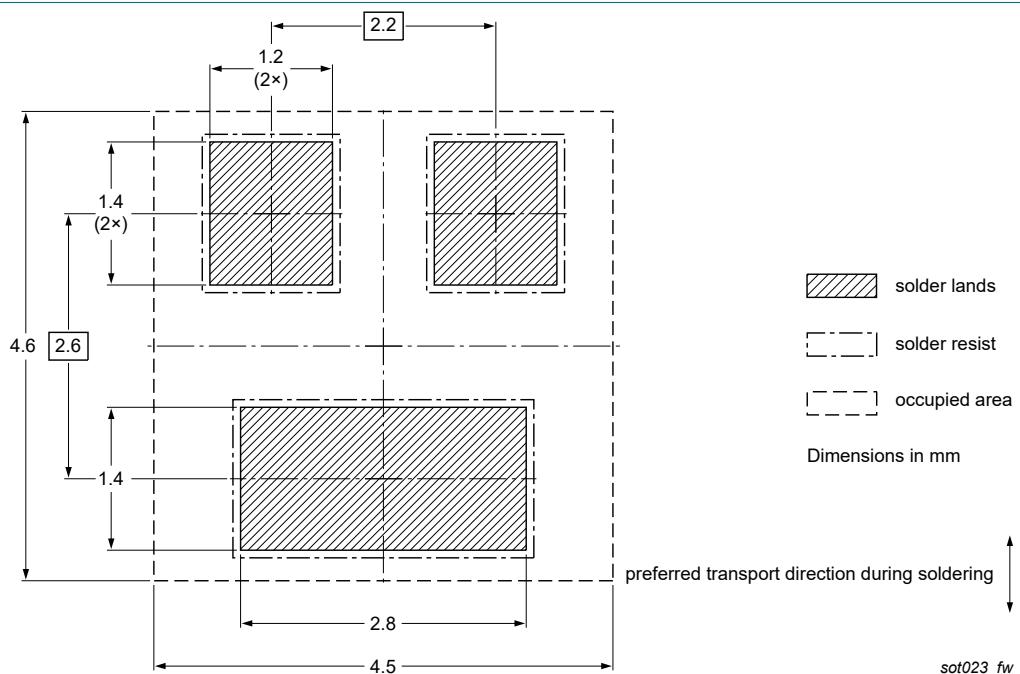


Fig. 12. Wave soldering footprint for SOT23

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4320T v.2	20250804	Product data sheet	-	PBSS4320T v.1
Modifications	<ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li></ul>			
PBSS4320T v.1	20031127	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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