



PBSS4520X

20 V, 5 A NPN low VCEsat transistor

1 October 2025

Product data sheet

1. General description

NPN low VCEsat transistor in a SOT89 (SC-62/TO-243) small and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5520X

2. Features and benefits

- High h_{FE} and low V_{CEsat} at high current operation
- High collector current capability: I_C maximum 5 A
- Higher efficiency leading to less heat generation

3. Applications

- Medium power peripheral drivers, e.g. fans and motors
- Strobe flash units for DSC and mobile phones
- Inverter applications, e.g. TFT displays
- Power switch for LAN and ADSL systems
- Medium power DC-to-DC conversion
- Battery chargers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	20	V
I_C	collector current		-	-	5	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms	-	-	10	A
R_{CEsat}	collector-emitter saturation resistance	$I_C = 5$ A; $I_B = 500$ mA; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	32	44	$m\Omega$

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		
2	C	collector		
3	B	base		

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6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS4520X	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PBSS4520X	%1F

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	20	V
V_{CEO}	collector-emitter voltage	open base		-	20	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
I_C	collector current			-	5	A
I_{CRM}	repetitive peak collector current		[1] [2]	-	7	A
I_{CM}	peak collector current	single pulse; $t_p \leq 1$ ms		-	10	A
I_B	base current			-	1	A
I_{BM}	peak base current	single pulse; $t_p \leq 1$ ms		-	2	A
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C	[2] [3]	-	2.5	W
			[2]	-	0.55	W
			[4]	-	1	W
			[5]	-	1.4	W
			[6]	-	1.6	W
				-	150	°C
T_j	junction temperature			-65	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Operated under pulsed conditions: pulse width $t_p \leq 10$ ms; duty cycle $\delta \leq 0.2$.

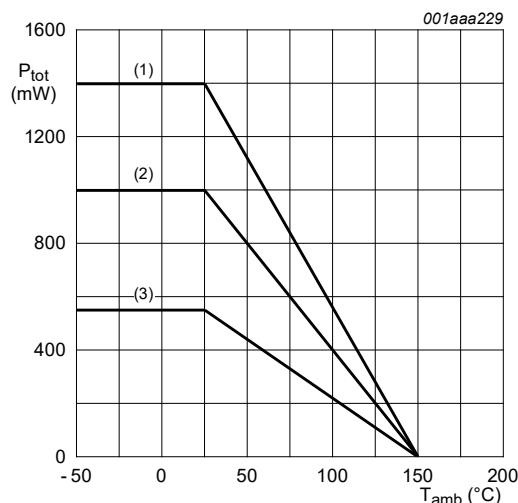
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Operated under pulsed conditions: $t_p \leq 10$ ms; $\delta \leq 0.2$.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[5] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[6] Device mounted on a 7 cm² ceramic PCB, 1 cm² single-sided copper and tin-plated.



(1) FR4 PCB; 6 cm² mounting pad for collector
 (2) FR4 PCB; 1 cm² mounting pad for collector
 (3) FR4; standard footprint

Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	50	K/W
			[1]	-	-	225	K/W
			[3]	-	-	125	K/W
			[4]	-	-	90	K/W
			[5]	-	-	80	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	16	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Operated under pulsed conditions: $t_p \leq 10$ ms; $\delta \leq 0.2$.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

[5] Device mounted on a 7 cm² ceramic PCB, 1 cm² single-sided copper and tin-plated.

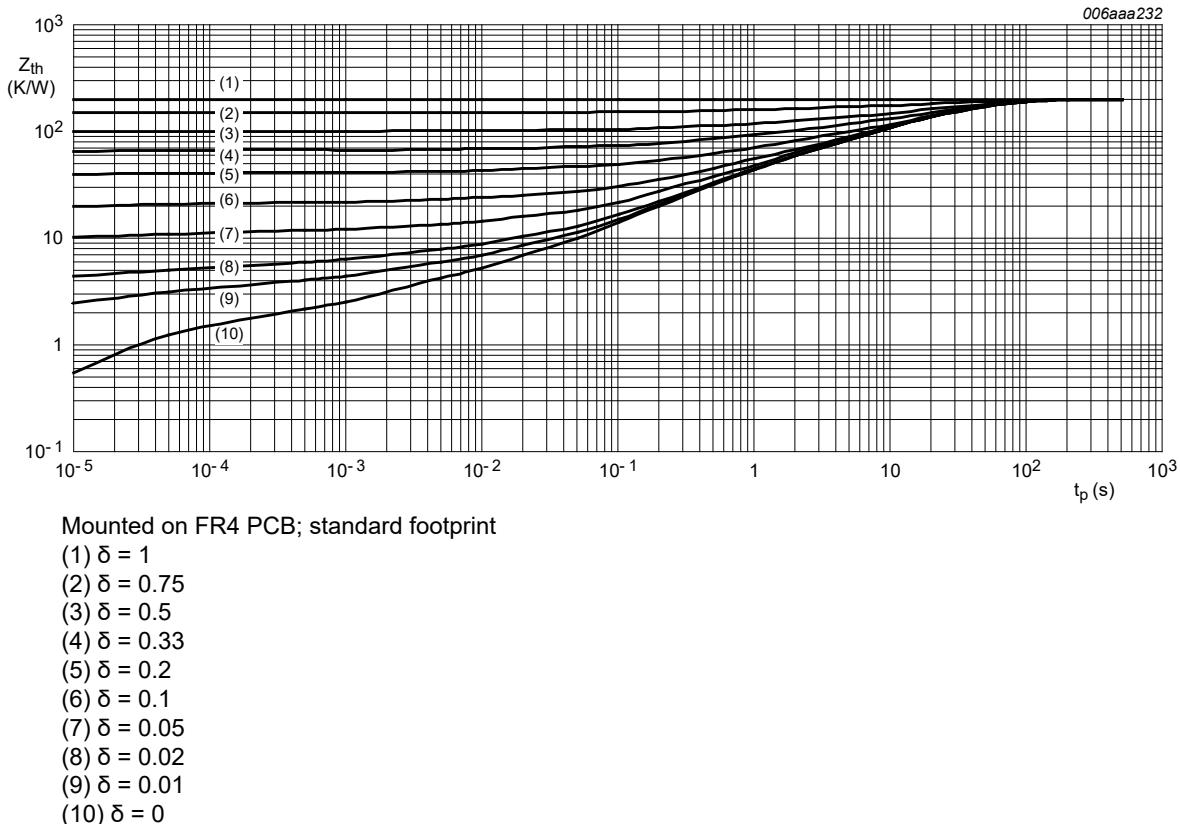


Fig. 2. Transient thermal impedance as a function of pulse duration; typical values

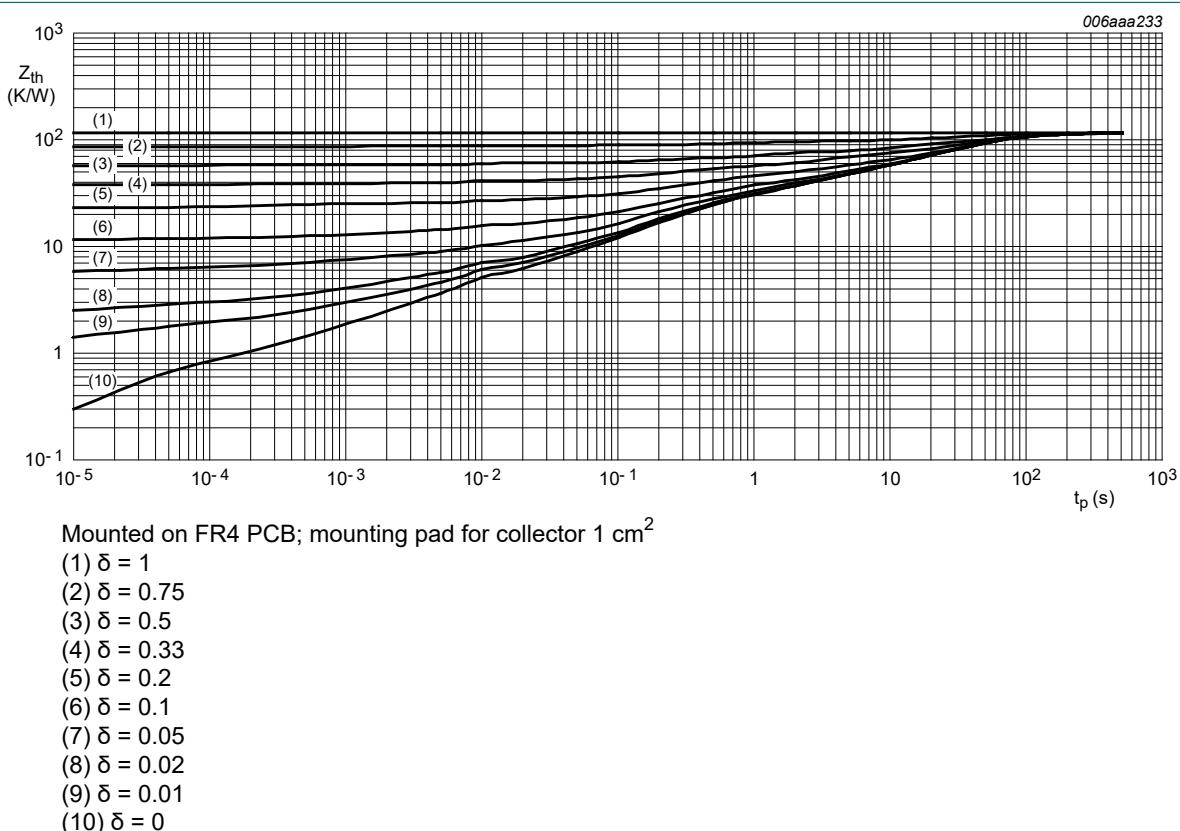


Fig. 3. Transient thermal impedance as a function of pulse duration; typical values

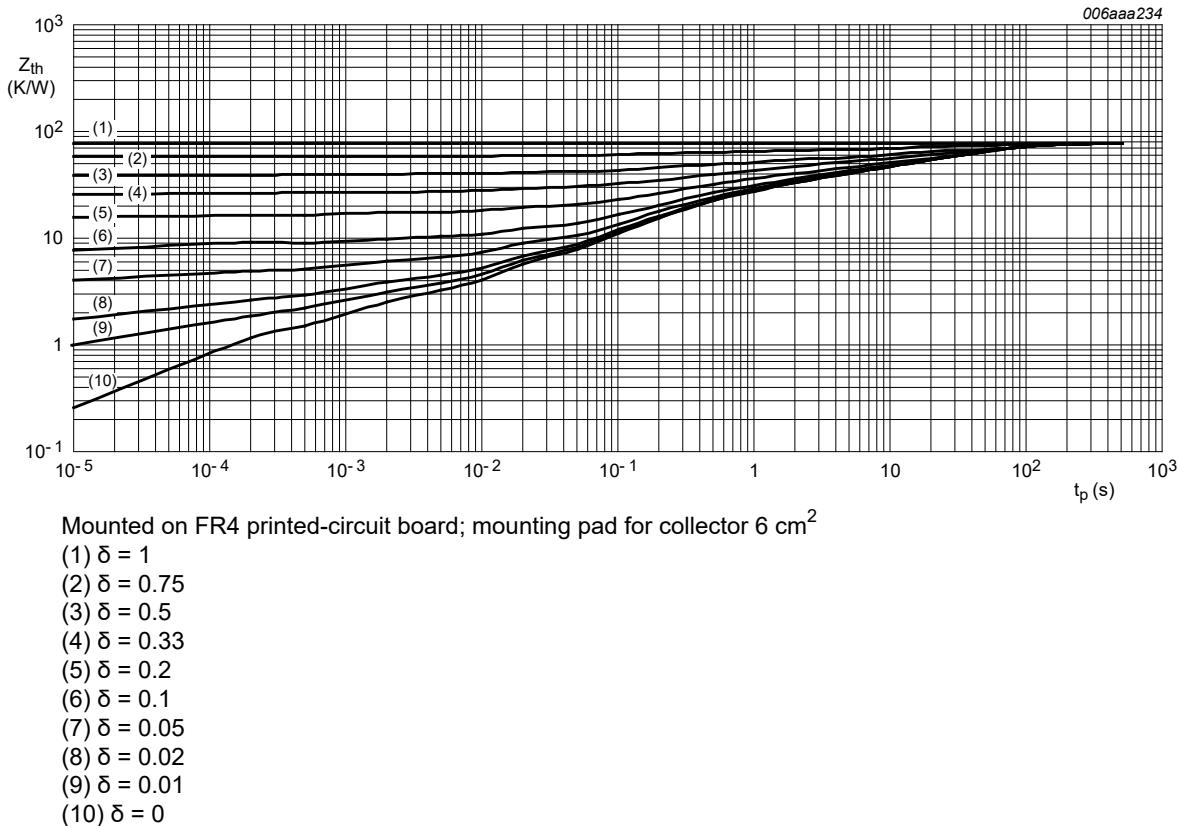
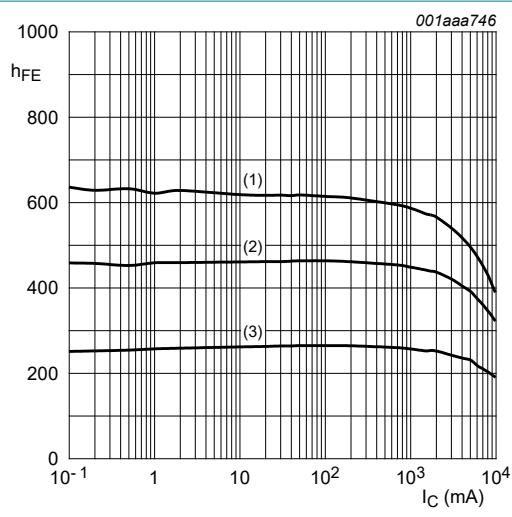


Fig. 4. Transient thermal impedance as a function of pulse duration; typical values

10. Characteristics

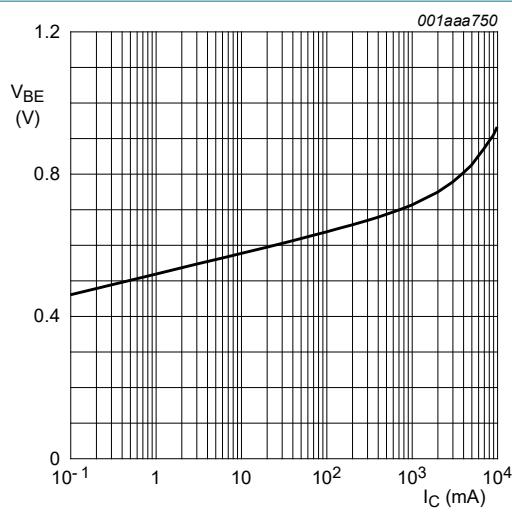
Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
		$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; T_j = 150 \text{ }^\circ\text{C}$	-	-	50	µA
I_{CES}	collector-emitter cut-off current	$V_{CE} = 20 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
I_{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_C = 0 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	-	100	nA
h_{FE}	DC current gain	$V_{CE} = 2 \text{ V}; I_C = 0.5 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	300	450	-	
		$V_{CE} = 2 \text{ V}; I_C = 1 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	300	440	-	
		$V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	250	420	-	
		$V_{CE} = 2 \text{ V}; I_C = 5 \text{ A}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	200	380	-	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 0.5 \text{ A}; I_B = 5 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	35	50	mV
		$I_C = 1 \text{ A}; I_B = 10 \text{ mA}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	50	70	mV
		$I_C = 2.5 \text{ A}; I_B = 125 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	85	120	mV
		$I_C = 4 \text{ A}; I_B = 200 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	130	180	mV
		$I_C = 5 \text{ A}; I_B = 500 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	160	220	mV
R_{CEsat}	collector-emitter saturation resistance		-	32	44	mΩ
V_{BEsat}	base-emitter saturation voltage	$I_C = 4 \text{ A}; I_B = 200 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.9	1.05	V
		$I_C = 5 \text{ A}; I_B = 500 \text{ mA}; \text{pulsed}; t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.96	1.1	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; I_C = 2 \text{ A}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	0.74	0.85	V
f_T	transition frequency	$V_{CE} = 10 \text{ V}; I_C = 100 \text{ mA}; f = 100 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$	100	125	-	MHz
C_c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^\circ\text{C}$	-	90	110	pF



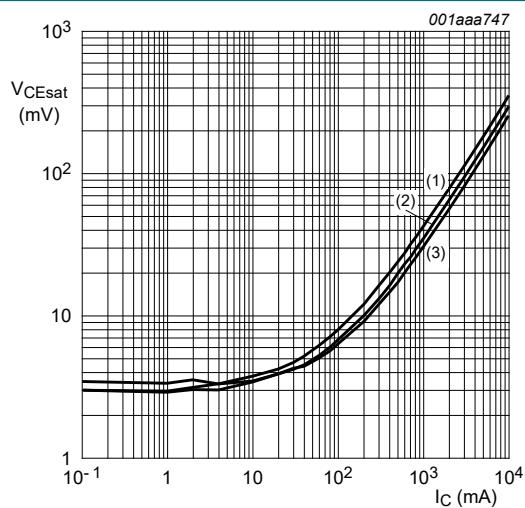
$V_{CE} = 2 \text{ V}$
 (1) $T_{amb} = 100 \text{ }^{\circ}\text{C}$
 (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
 (3) $T_{amb} = -55 \text{ }^{\circ}\text{C}$

Fig. 5. DC current gain as a function of collector current; typical values



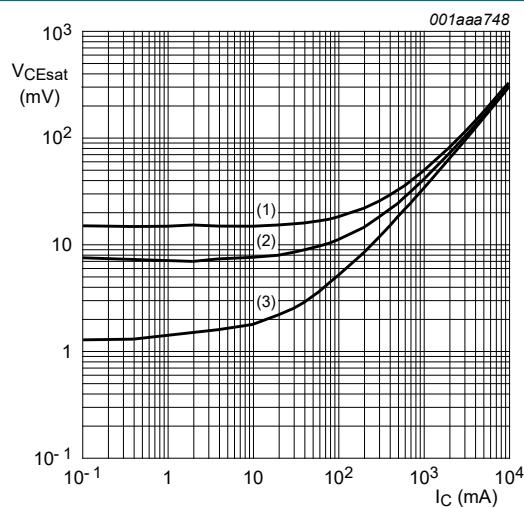
$V_{CE} = 2 \text{ V}$
 $T_{amb} = 25 \text{ }^{\circ}\text{C}$

Fig. 6. Base-emitter voltage as a function of collector current; typical values



$I_c/I_b = 20$
 (1) $T_{amb} = 100 \text{ }^{\circ}\text{C}$
 (2) $T_{amb} = 25 \text{ }^{\circ}\text{C}$
 (3) $T_{amb} = -55 \text{ }^{\circ}\text{C}$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values



$T_{amb} = 25 \text{ }^{\circ}\text{C}$
 (1) $I_c/I_b = 100$
 (2) $I_c/I_b = 50$
 (3) $I_c/I_b = 10$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

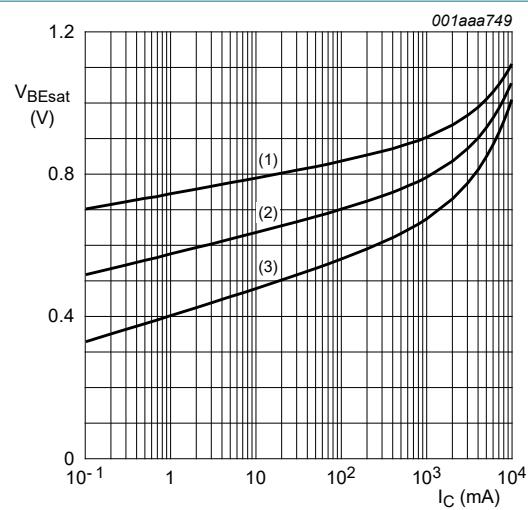


Fig. 9. Base-emitter saturation voltage as a function of collector current; typical values

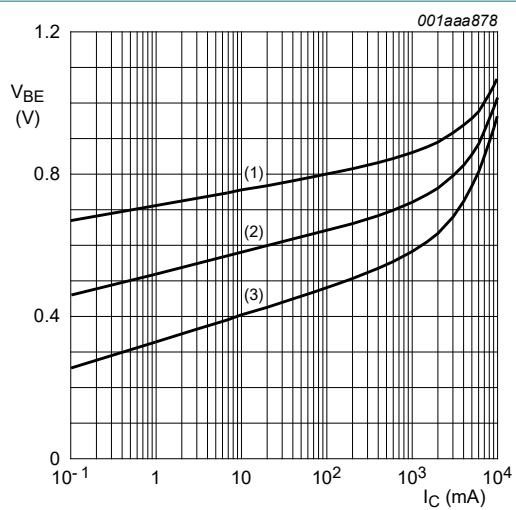


Fig. 10. Base-emitter voltage as a function of collector current; typical values

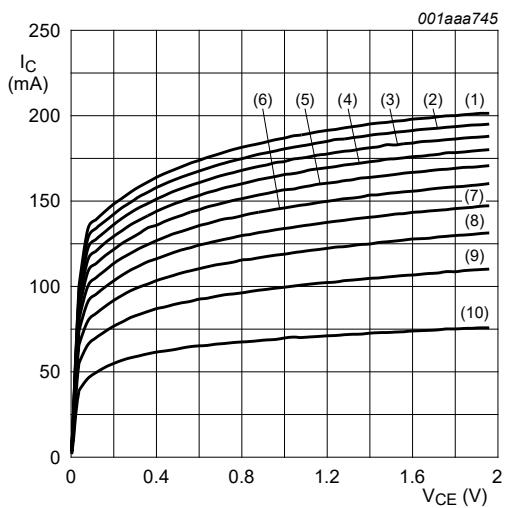


Fig. 11. Collector current as a function of collector-emitter voltage; typical values

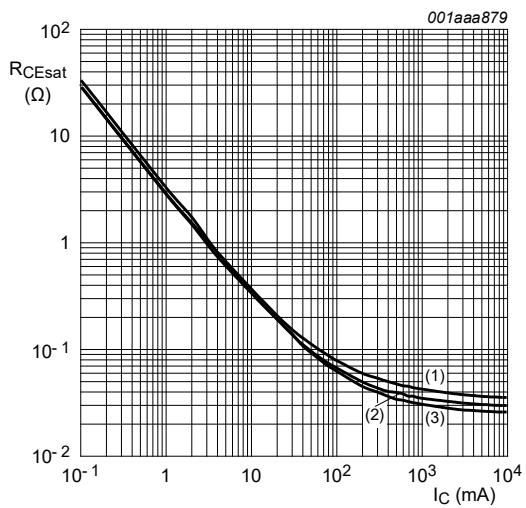


Fig. 12. Equivalent on-resistance as a function of collector current; typical values

11. Package outline

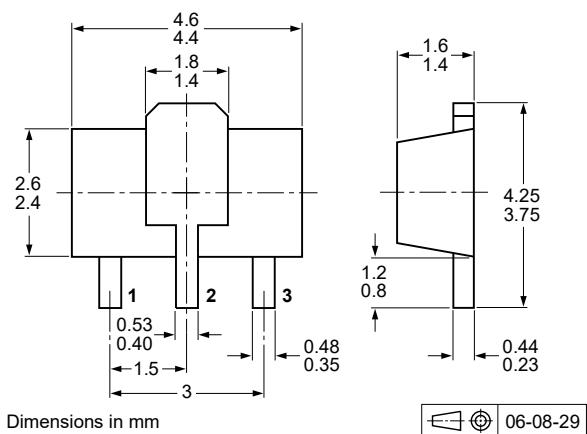


Fig. 13. Package outline SOT89

12. Soldering

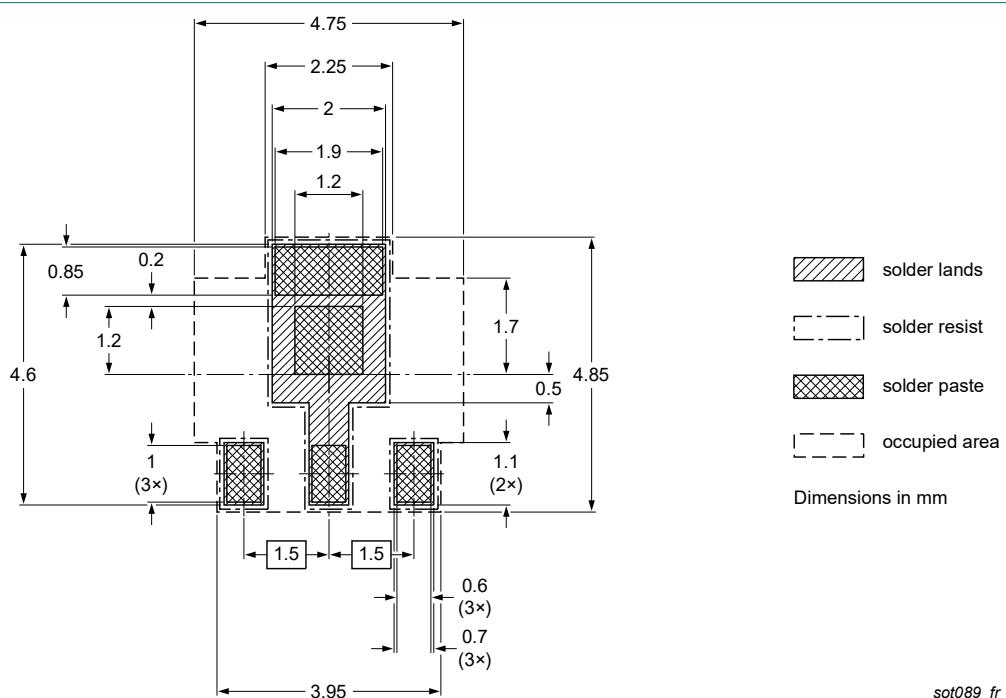


Fig. 14. Reflow soldering footprint for SOT89

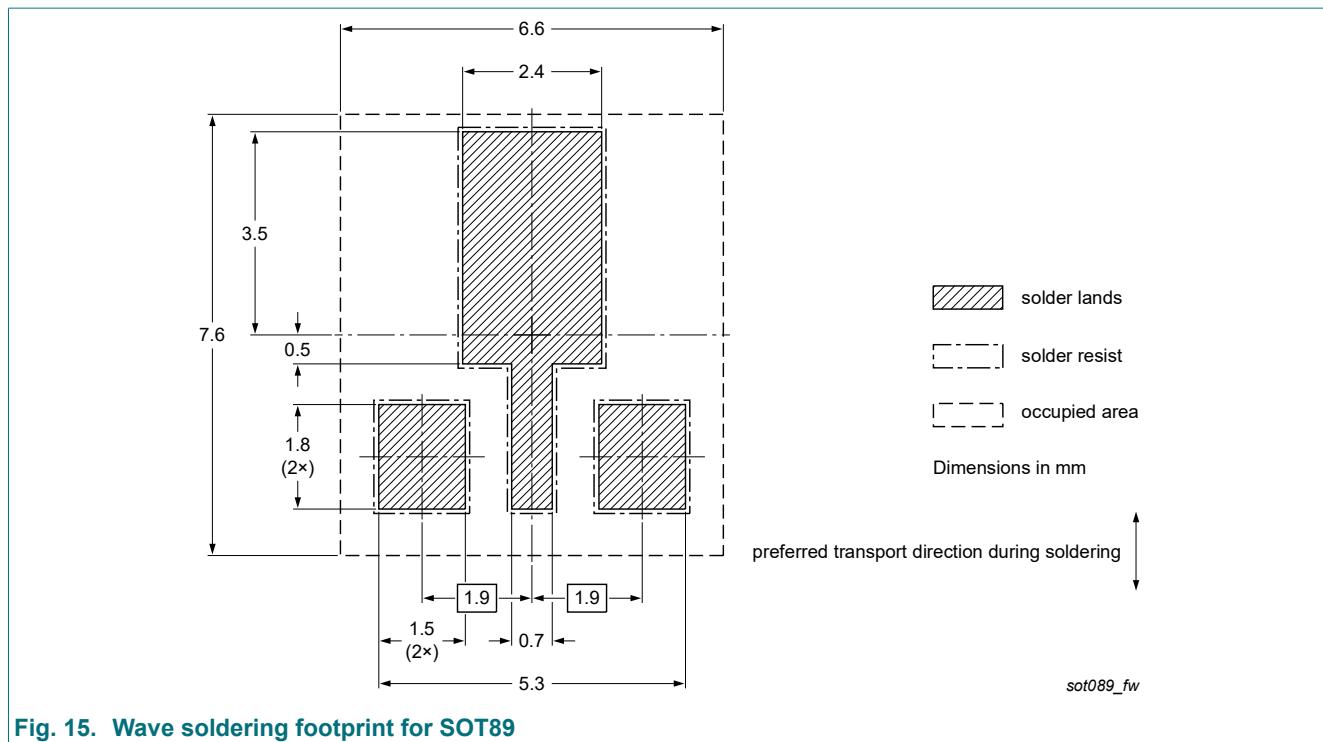


Fig. 15. Wave soldering footprint for SOT89

13. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4520X v.4	20251001	Product data sheet	-	PBSS4520X v.3
Modifications:	<ul style="list-style-type: none">Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
PBSS4520X v.3	20240119	Product data sheet	-	PBSS4520X v.2
PBSS4520X v.2	20041108	Product data sheet	-	PBSS4520X v.1
PBSS4520X v.1	20040611	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	1
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	2
9. Thermal characteristics.....	3
10. Characteristics.....	6
11. Package outline.....	9
12. Soldering.....	9
13. Revision history.....	11
14. Legal information.....	12

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