



# PESD5V0S1USF

Unidirectional ESD protection diode

9 September 2025

Product data sheet

## 1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode designed to protect one signal line from the damage caused by ESD and other transients. The device is encapsulated in a leadless super small DSN0603-2 (SOD962) Surface-Mounted Device (SMD) package.

## 2. Features and benefits

- ESD protection of one line
- Super small SMD package
- Ultra low leakage current  $I_{RM} < 1 \text{ nA}$
- ESD protection up to 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61643-321 (surge);  $I_{PPM} = 3.5 \text{ A}$

## 3. Applications

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- Communication systems
- Portable electronics

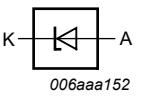
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	-	5	V
$C_d$	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 \text{ }^{\circ}\text{C}$	-	35	42	pF

## 5. Pinning information

**Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		
2	A	anode	 <b>DSN0603-2 (SOD962)</b>	 <b>006aaa152</b>

[1] The marking bar indicates the cathode.

## 6. Ordering information

**Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
PESD5V0S1USF	DSN0603-2	silicon, leadless ultra small package; 2 terminals; 0.4 mm pitch; 0.6 x 0.3 x 0.3 mm body	SOD962

## 7. Marking

**Table 4. Marking codes**

Type number	Marking code
PESD5V0S1USF	1

## 8. Limiting values

**Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>PPM</sub>	rated peak pulse power	$t_p = 8/20 \mu s$	[1]	-	35	W
I <sub>PPM</sub>	rated peak pulse current		[1]	-	3.5	A
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

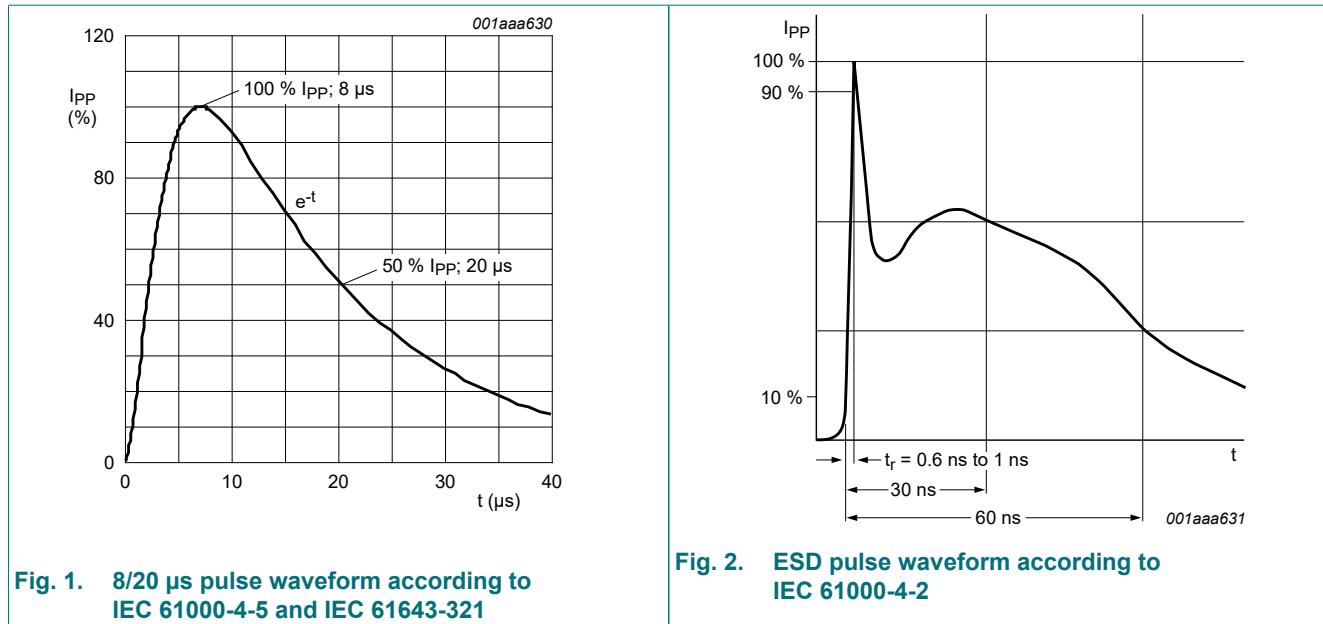
  

ESD maximum ratings						
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge); T <sub>amb</sub> = 25 °C	[2] [3]	-	30	kV
		IEC 61000-4-2 (air discharge); T <sub>amb</sub> = 25 °C	[2] [3]	-	30	kV
		machine model; T <sub>amb</sub> = 25 °C	[3]	-	400	V
		MIL-STD-883 (human body model); T <sub>amb</sub> = 25 °C		-	10	kV

[1] Device stressed with 8/20  $\mu s$  exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

[2] Device stressed with ten non-repetitive ESD pulses.

[3] Measured from pin 1 to pin 2.



## 9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$V_{RWM}$	reverse standoff voltage	$T_{amb} = 25^\circ C$		-	-	5	V
$V_{BR}$	breakdown voltage	$I_R = 1 \text{ mA}; T_{amb} = 25^\circ C$		6	7	8	V
$I_{RM}$	reverse leakage current	$V_{RWM} = 5 \text{ V}; T_{amb} = 25^\circ C$		-	1	100	nA
$C_d$	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25^\circ C$		-	35	42	pF
$V_{CL}$	clamping voltage	$I_{PP} = 1 \text{ A}; T_{amb} = 25^\circ C$	[1] [2]	-	-	9	V
		$I_{PPM} = 3.5 \text{ A}; T_{amb} = 25^\circ C$	[1] [2]	-	-	11	V
$R_{dyn}$	dynamic resistance	$I_R = 10 \text{ A}; T_{amb} = 25^\circ C$	[3]	-	0.7	-	$\Omega$

[1] Device stressed with 8/20  $\mu\text{s}$  exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

[2] Measured from pin 1 to pin 2.

[3] Non-repetitive current pulse, Transmission Line Pulse (TLP)  $t_p = 100 \text{ ns}$ ; square pulse; ANSI/ESD STM5.5.1-2008.

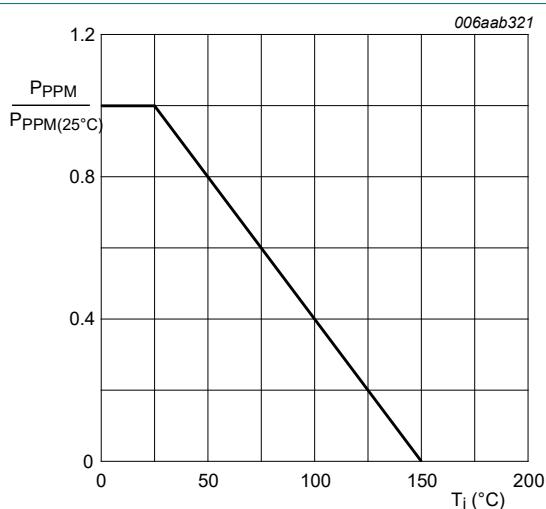


Fig. 3. Relative variation of rated peak pulse power as a function of junction temperature; typical values

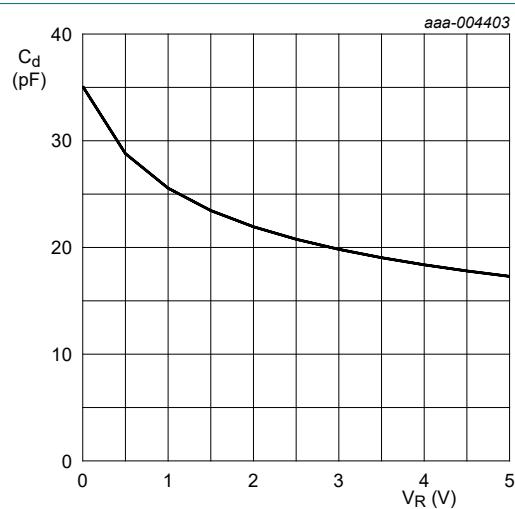


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

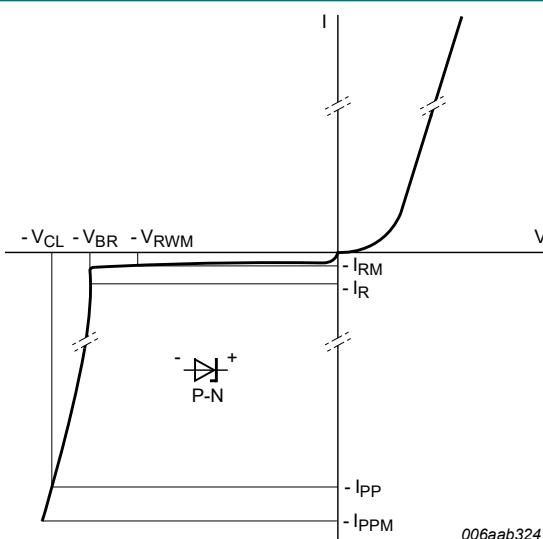


Fig. 5. V-I characteristics for a unidirectional ESD protection diode

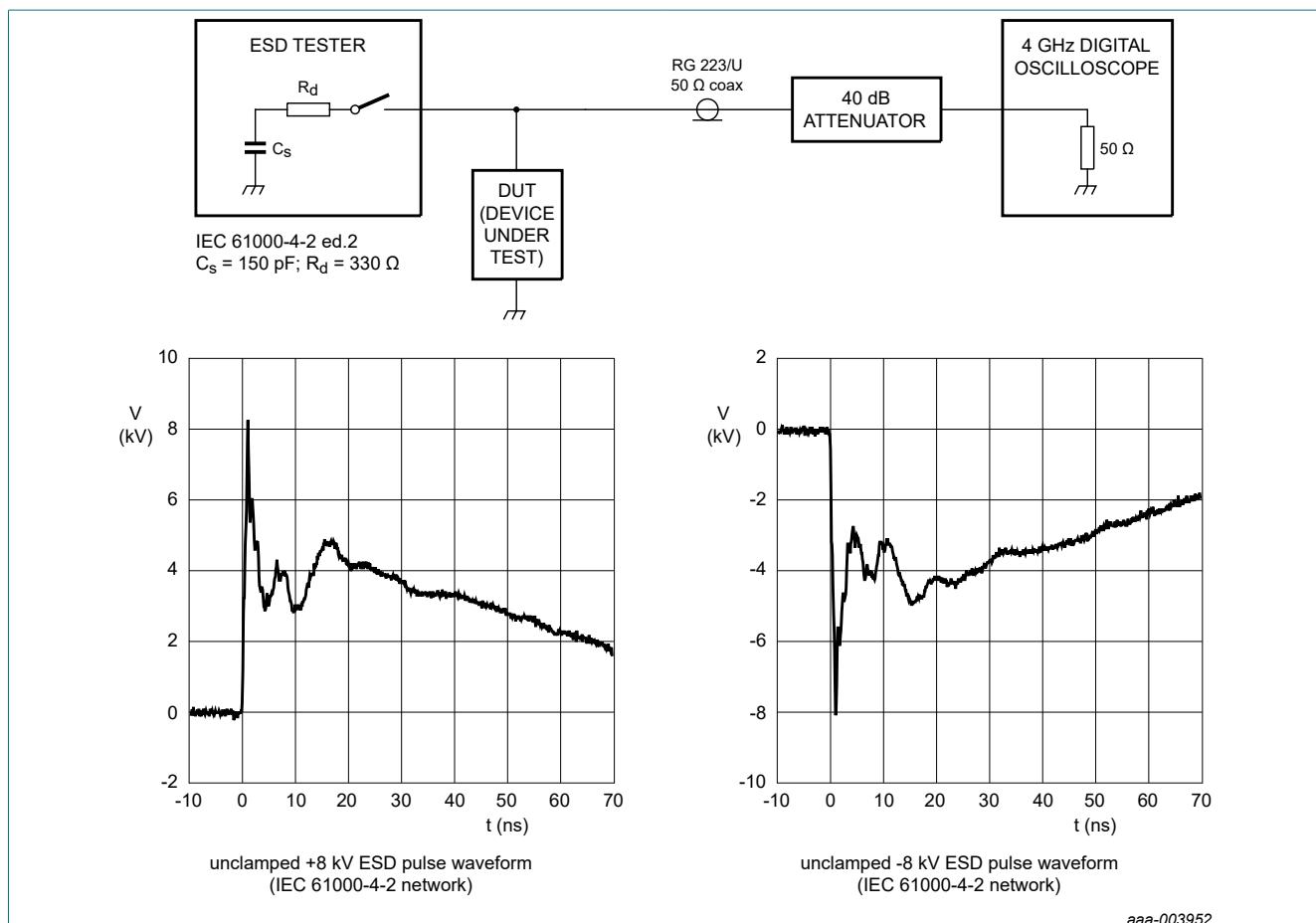
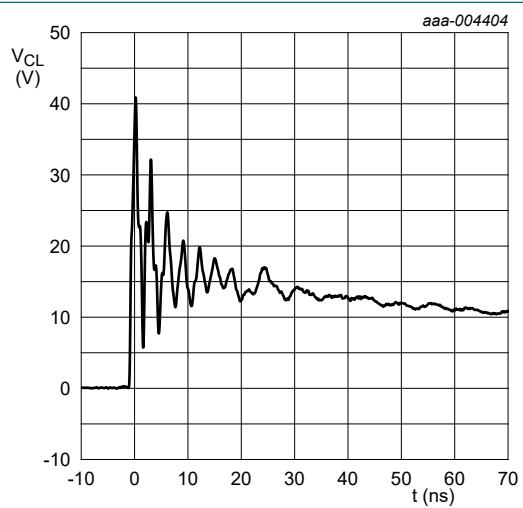
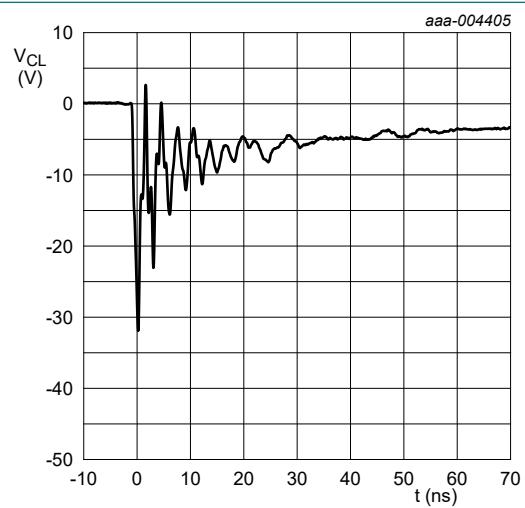


Fig. 6. ESD clamping test setup and waveforms

Fig. 7. Clamped +8 kV pulse waveform  
(IEC 61000-4-2 network)Fig. 8. Clamped -8 kV pulse waveform  
(IEC 61000-4-2 network)

## 10. Application information

The device is designed for the protection of one unidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

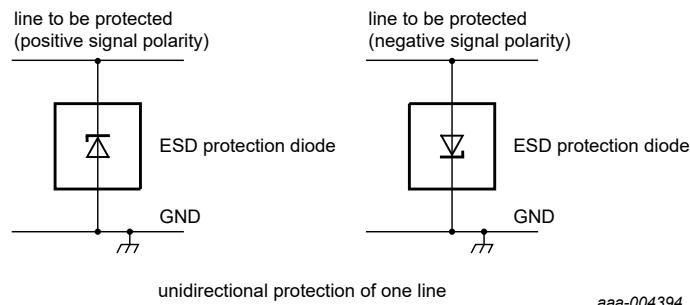


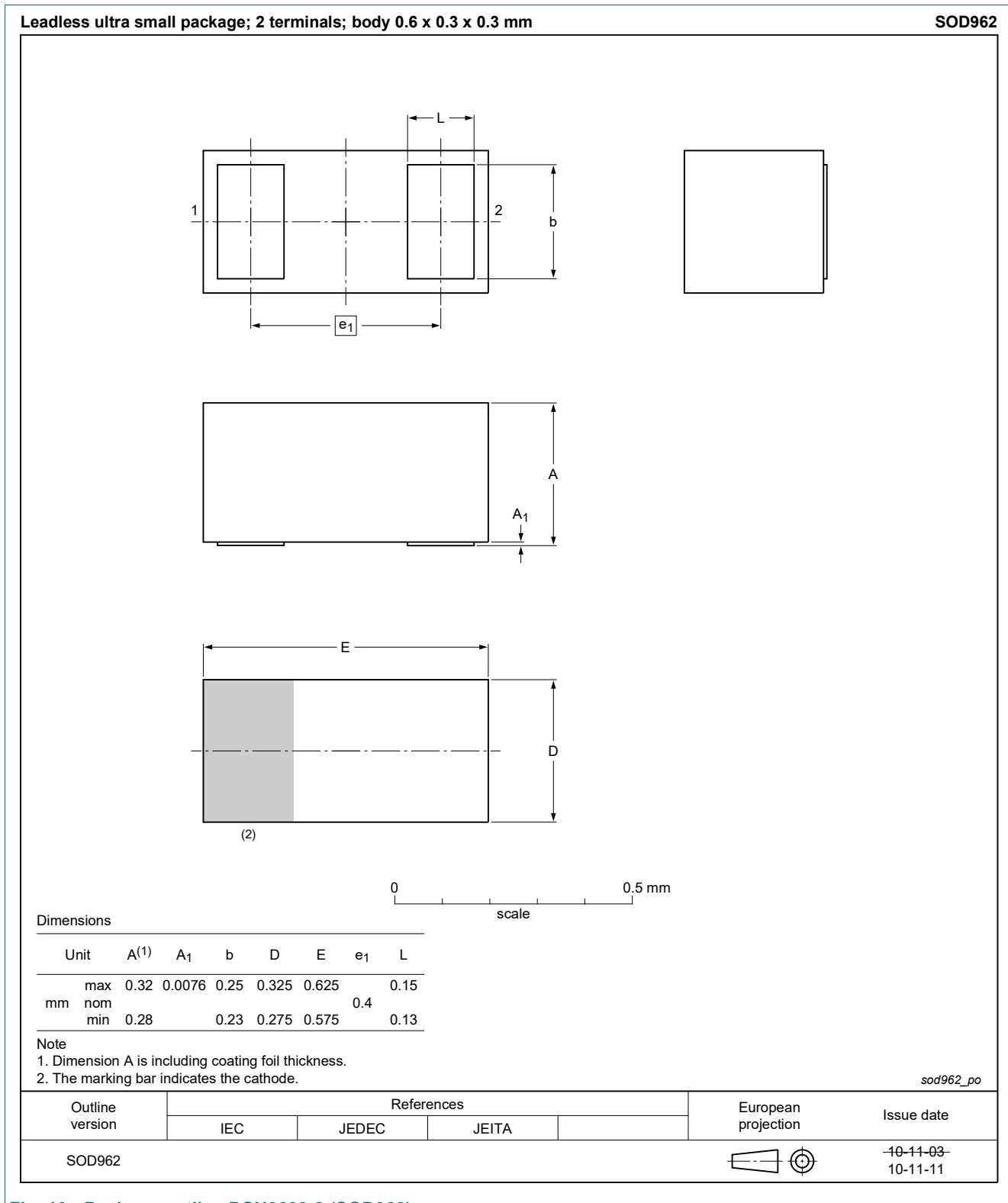
Fig. 9. Application diagram

### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

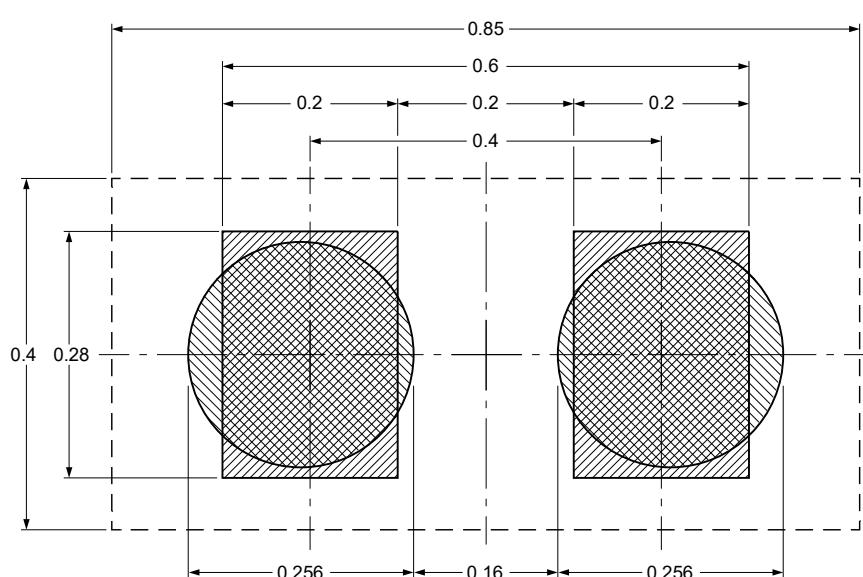
## 11. Package outline



## 12. Soldering

Footprint information for reflow soldering of leadless ultra small package; 2 terminals

SOD962



recommended stencil thickness: 0.1 mm

occupied area

solder paste

solder lands

Dimensions in mm

Issue date 16-01-26  
20-09-15

sod962\_fr

Fig. 11. Reflow soldering footprint for DSN0603-2 (SOD962)

## 13. Revision history

**Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5V0S1USF v.2	20250909	Product data sheet	-	PESD5V0S1USF v.1
Modifications:	<ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li><li>Chapter "Soldering": Figure updated.</li></ul>			
PESD5V0S1USF v.1	20120716	Product data sheet	-	-

## 14. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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## Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	3
9. Characteristics.....	4
10. Application information.....	6
11. Package outline.....	7
12. Soldering.....	8
13. Revision history.....	9
14. Legal information.....	10

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