



RB521CS30L

100 mA low VF Schottky barrier rectifier

2 October 2025

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD882 leadless ultra small Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \leq 100 \text{ mA}$
- Reverse voltage: $V_R \leq 30 \text{ V}$
- Low forward voltage: $V_F \leq 350 \text{ mV}$
- Low reverse current: $I_R \leq 10 \text{ } \mu\text{A}$
- Leadless ultra small SMD plastic package

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$I_{F(AV)}$	average forward current	$\delta = 0.5; f = 20 \text{ kHz}; \text{square wave}; T_{\text{amb}} \leq 135 \text{ } ^\circ\text{C}$	[1]	-	-	100	mA
		$\delta = 0.5; f = 20 \text{ kHz}; \text{square wave}; T_{\text{sp}} \leq 145 \text{ } ^\circ\text{C}$		-	-	100	mA
I_R	reverse current	$V_R = 10 \text{ V}$		-	2	10	μA
V_R	reverse voltage	$T_j = 25 \text{ } ^\circ\text{C}$		-	-	30	V
V_F	forward voltage	$I_F = 10 \text{ mA}$	[2]	-	280	350	mV

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm^2 .

[2] Pulse test: $t_p \leq 300 \text{ } \mu\text{s}; \delta \leq 0.02$.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 Transparent top view	 aaa-003679
2	A	anode		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package			Version
	Name	Description	Version	
RB521CS30L	DFN1006-2	plastic, leadless ultra small package; 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.48 mm body	SOD882	

7. Marking

Table 4. Marking codes

Type number	Marking code
RB521CS30L	AR

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage	$T_j = 25^\circ\text{C}$		-	30	V
$I_{F(AV)}$	average forward current	$\delta = 0.5$; $f = 20\text{ kHz}$; square wave; $T_{amb} \leq 135^\circ\text{C}$	[1]	-	100	mA
		$\delta = 0.5$; $f = 20\text{ kHz}$; square wave; $T_{sp} \leq 145^\circ\text{C}$		-	100	mA
I_{FSM}	non-repetitive peak forward current	$t_p \leq 8.3\text{ ms}$; half sine wave	[2]	-	3	A
P_{tot}	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$	[3] [4]	-	315	mW
			[1] [4]	-	565	mW
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm^2 .

[2] $T_j = 25^\circ\text{C}$ prior to surge.

[3] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

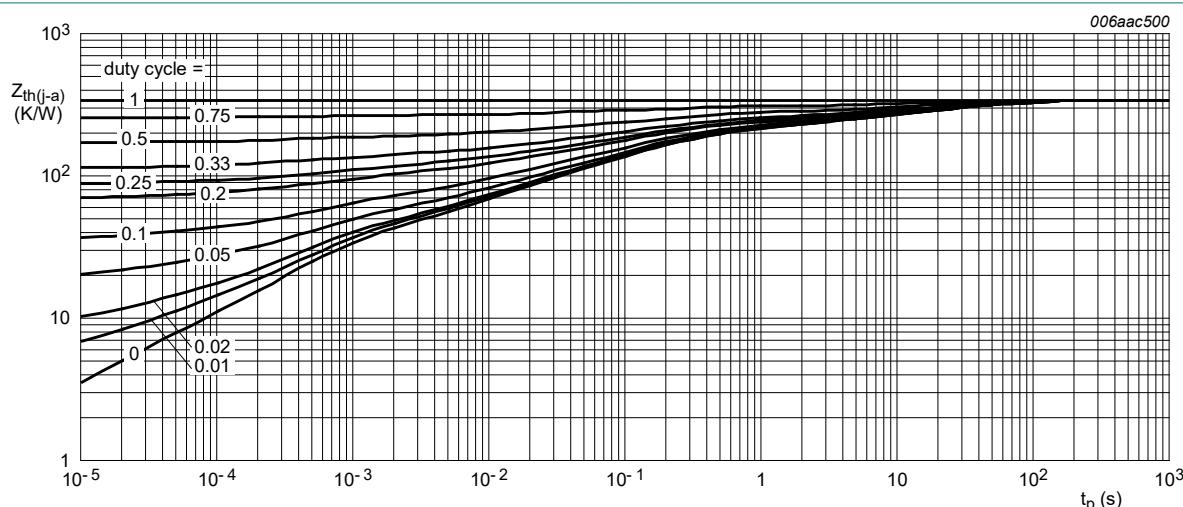
[4] Reflow soldering is the only recommended soldering method.

9. Thermal characteristics

Table 6. Thermal characteristics

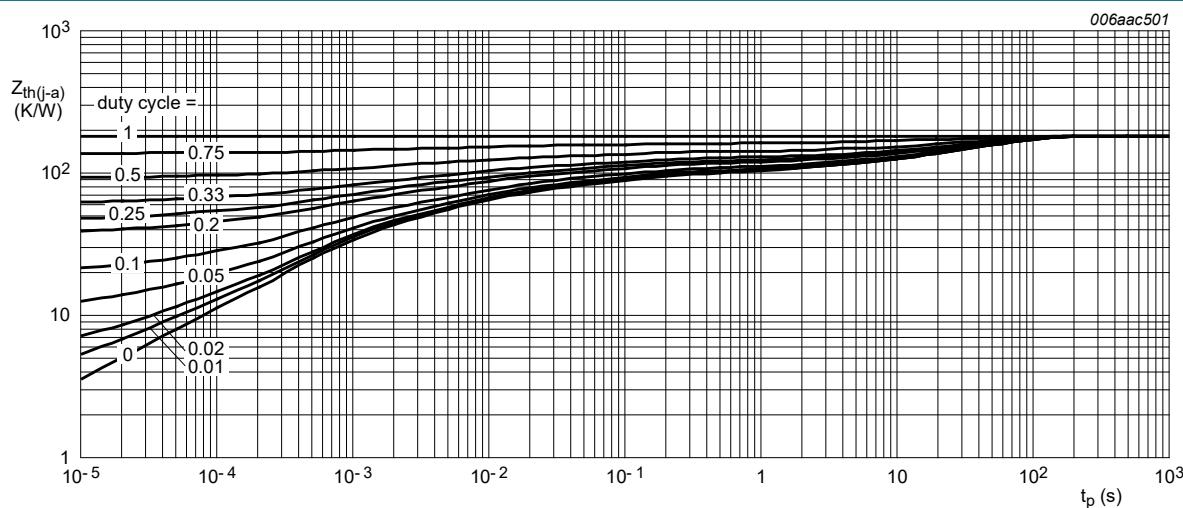
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	395	K/W
			[3]				
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[1] [2]	-	-	220	K/W
			[4]				
			[5]	-	-	70	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
 [2] Reflow soldering is the only recommended soldering method.
 [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
 [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
 [5] Soldering point of cathode tab.



FR4 PCB, standard footprint

Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm²

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

$T_{amb} = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 0.1 \text{ mA}$	[1]	-	145	-	mV
		$I_F = 1 \text{ mA}$	[1]	-	210	-	mV
		$I_F = 10 \text{ mA}$	[1]	-	280	350	mV
		$I_F = 100 \text{ mA}$	[1]	-	405	-	mV
I_R	reverse current	$V_R = 10 \text{ V}$		-	2	10	μA
C_d	diode capacitance	$V_R = 1 \text{ V}; f = 1 \text{ MHz}$		-	8	-	pF

[1] Pulse test: $t_p \leq 300 \mu\text{s}; \delta \leq 0.02$.

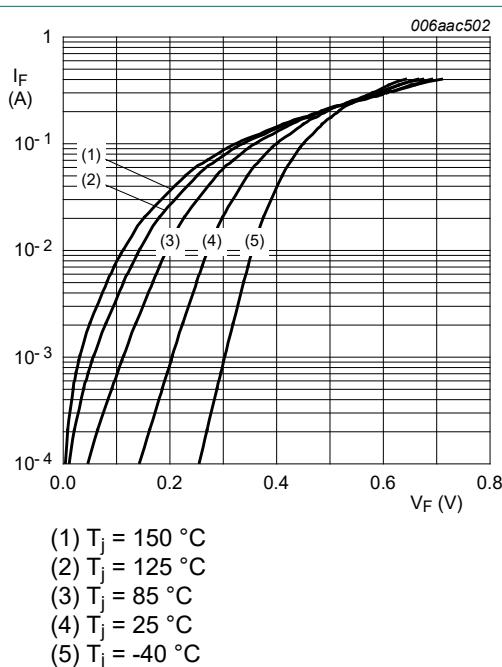


Fig. 3. Forward current as a function of forward voltage; typical values

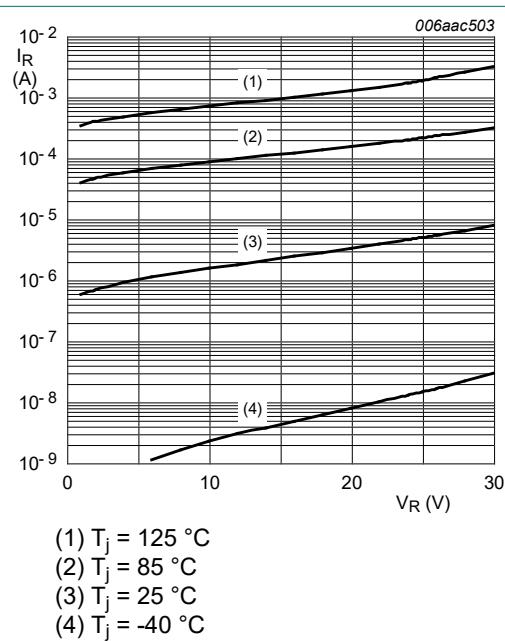
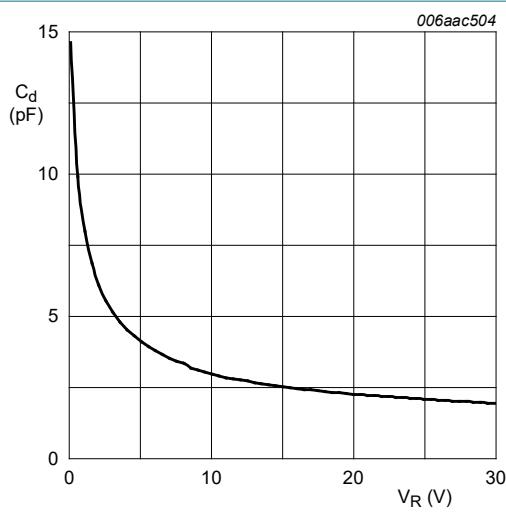
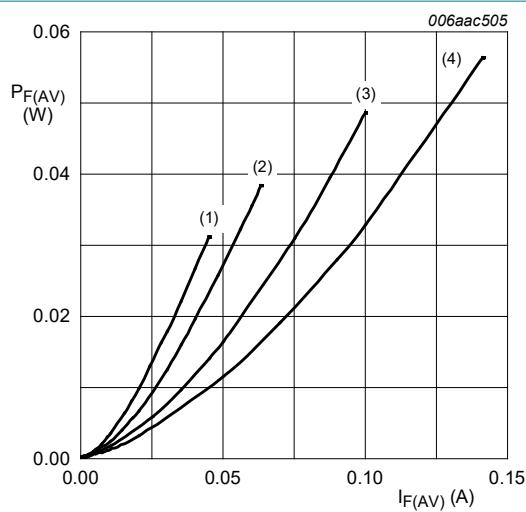


Fig. 4. Reverse current as a function of reverse voltage; typical values



$f = 1$ MHz; $T_{amb} = 25$ °C

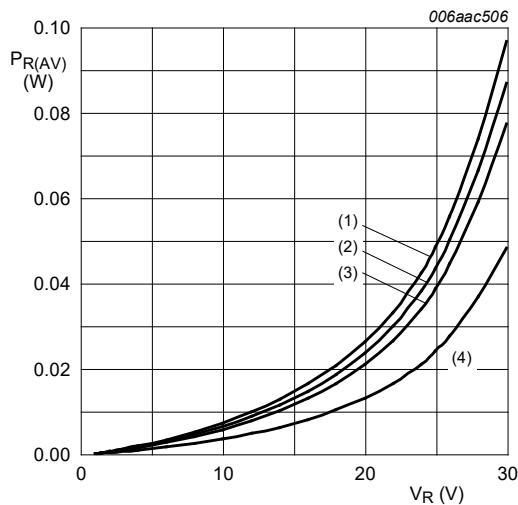
Fig. 5. Diode capacitance as a function of reverse voltage; typical values



$T_j = 150$ °C

- (1) $\delta = 0.1$
- (2) $\delta = 0.2$
- (3) $\delta = 0.5$
- (4) $\delta = 1$

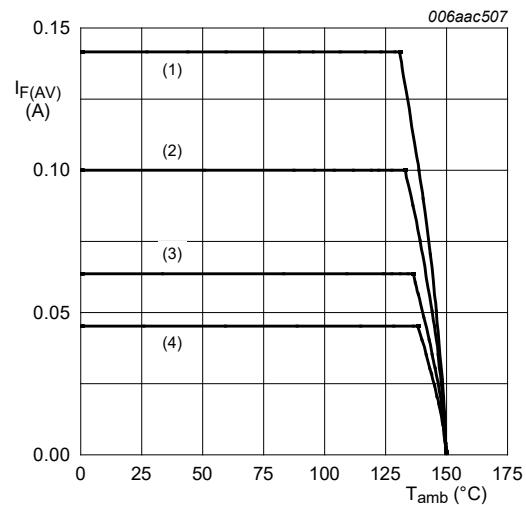
Fig. 6. Average forward power dissipation as a function of average forward current; typical values



$T_j = 125$ °C

- (1) $\delta = 1$; DC
- (2) $\delta = 0.9$; $f = 20$ kHz
- (3) $\delta = 0.8$; $f = 20$ kHz
- (4) $\delta = 0.5$; $f = 20$ kHz

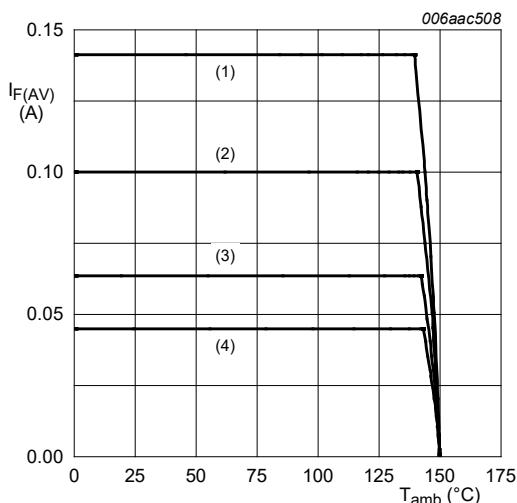
Fig. 7. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

- $T_j = 150$ °C
- (1) $\delta = 1$; DC
- (2) $\delta = 0.5$; $f = 20$ kHz
- (3) $\delta = 0.2$; $f = 20$ kHz
- (4) $\delta = 0.1$; $f = 20$ kHz

Fig. 8. Average forward current as a function of ambient temperature; typical values



FR4 PCB, mounting pad for cathode 1 cm²

$T_j = 150$ °C

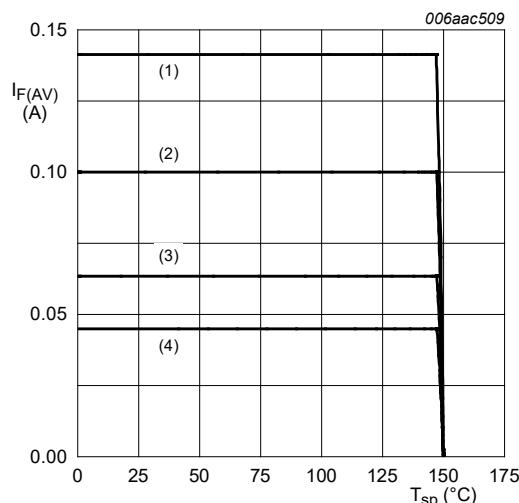
(1) $\delta = 1$; DC

(2) $\delta = 0.5$; $f = 20$ kHz

(3) $\delta = 0.2$; $f = 20$ kHz

(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



$T_j = 150$ °C

(1) $\delta = 1$; DC

(2) $\delta = 0.5$; $f = 20$ kHz

(3) $\delta = 0.2$; $f = 20$ kHz

(4) $\delta = 0.1$; $f = 20$ kHz

Fig. 10. Average forward current as a function of solder point temperature; typical values

11. Test information

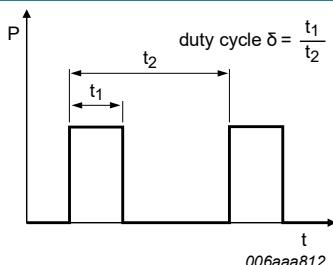


Fig. 11. Duty cycle definition

The current ratings for the typical waveforms are calculated according to the equations:

$I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

$I_{RMS} = I_{F(AV)}$ at DC

$I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

12. Package outline

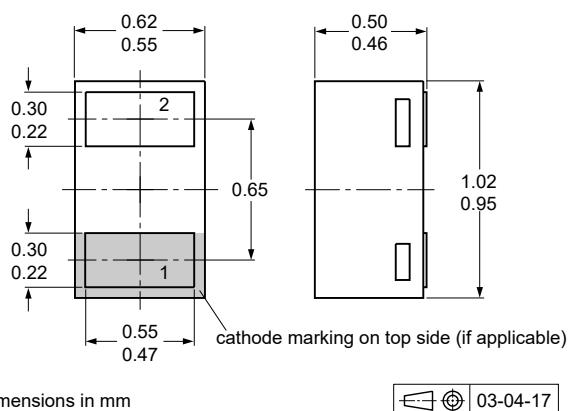


Fig. 12. Package outline DFN1006-2 (SOD882)

13. Soldering

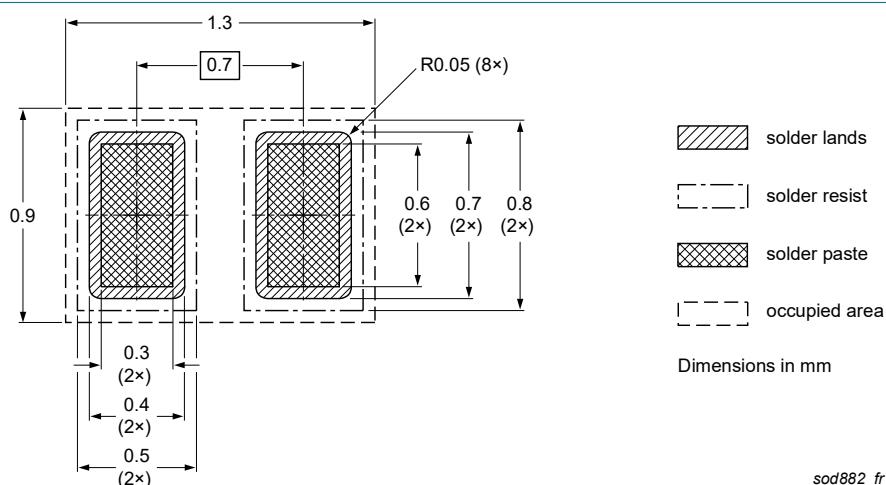


Fig. 13. Reflow soldering footprint for DFN1006-2 (SOD882)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
RB521CS30L v.2	20251002	Product data sheet	-	RB521CS30L v.1
Modifications:	<ul style="list-style-type: none">Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).			
RB521CS30L v.1	20110124	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description.....	1
2. Features and benefits.....	1
3. Applications.....	1
4. Quick reference data.....	1
5. Pinning information.....	2
6. Ordering information.....	2
7. Marking.....	2
8. Limiting values.....	2
9. Thermal characteristics.....	3
10. Characteristics.....	4
11. Test information.....	6
12. Package outline.....	7
13. Soldering.....	7
14. Revision history.....	8
15. Legal information.....	9

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